

## PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2000-235542  
 (43)Date of publication of application : 29.08.2000

(51)Int.Cl. G06F 13/14  
 G06F 12/02  
 G06F 12/10  
 G06F 13/28  
 G06F 13/36  
 G11B 20/10

(21)Application number : 11-037631  
 (22)Date of filing : 16.02.1999

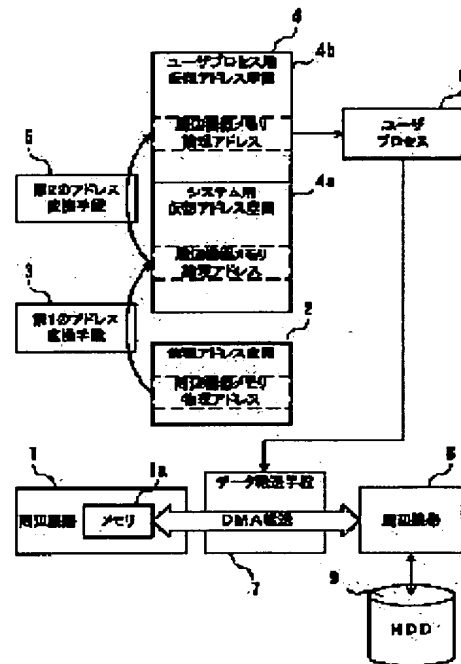
(71)Applicant : SONY CORP  
 (72)Inventor : KON MASASHI  
 NAKAMURA RYUICHI

## (54) DATA PROCESSOR AND RECORDING MEDIUM

## (57)Abstract:

PROBLEM TO BE SOLVED: To speedily transfer data transfer between plural peripheral equipments while utilizing the functions of a file management system.

SOLUTION: A 1st address conversion means 3 converts a physical address of a peripheral equipment 1 into a logical address of a system virtual address space 4a formed in a virtual address space 4. A 2nd address conversion means 5 converts the logical address converted by the means 3 into a logical address in a user processor virtual address space 4b corresponding to a specific user process 6 executed on an OS. A data transfer means 7 executes data transfer based on a direct memory access between a memory 1a of the equipment 1 and another peripheral equipment 8 based on a data transfer request outputted from the user process 6 and specifying the logical address converted by the means 5.



LEGAL STATUS

[Date of request for examination]  
[Date of sending the examiner's decision of rejection]  
[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]  
[Date of final disposal for application]  
[Patent number]  
[Date of registration]  
[Number of appeal against examiner's decision of rejection]  
[Date of requesting appeal against examiner's decision of rejection]  
[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office